

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re the Application of: AKAMATSU, et al.

Serial No.: 09/392,722

Group Art Unit: 2827

Filed: September 9, 1999

Examiner: GRAYBILL, David E.

For: INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION WITH CIRCUIT BOARD AND FABRICATION METHOD THEREOF

P.T.O. Confirmation No.: 1679

NOTICE OF APPEAL

Commissioner for Patents
Washington, D.C. 20231

Date: April 8, 2003

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated **October 8, 2002** of the Primary Examiner.

The item(s) checked below are appropriate:

1. XX A petition for a third month extension of time to respond to the Final Rejection is filed on herewith.
2. XX A timely response to the Final Rejection has been filed, as provided in 37 C.F.R. 1.113.
3. XX Fee -\$320.00 or Small Entity -\$160.00

XX Enclosed
 Not required (fee paid in prior appeal)
 Charge to Deposit Account No. 01-2340

In the event this Notice of Appeal is not considered to be timely filed, Applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this Notice.


Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

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